

FIG.1(Prior Art)

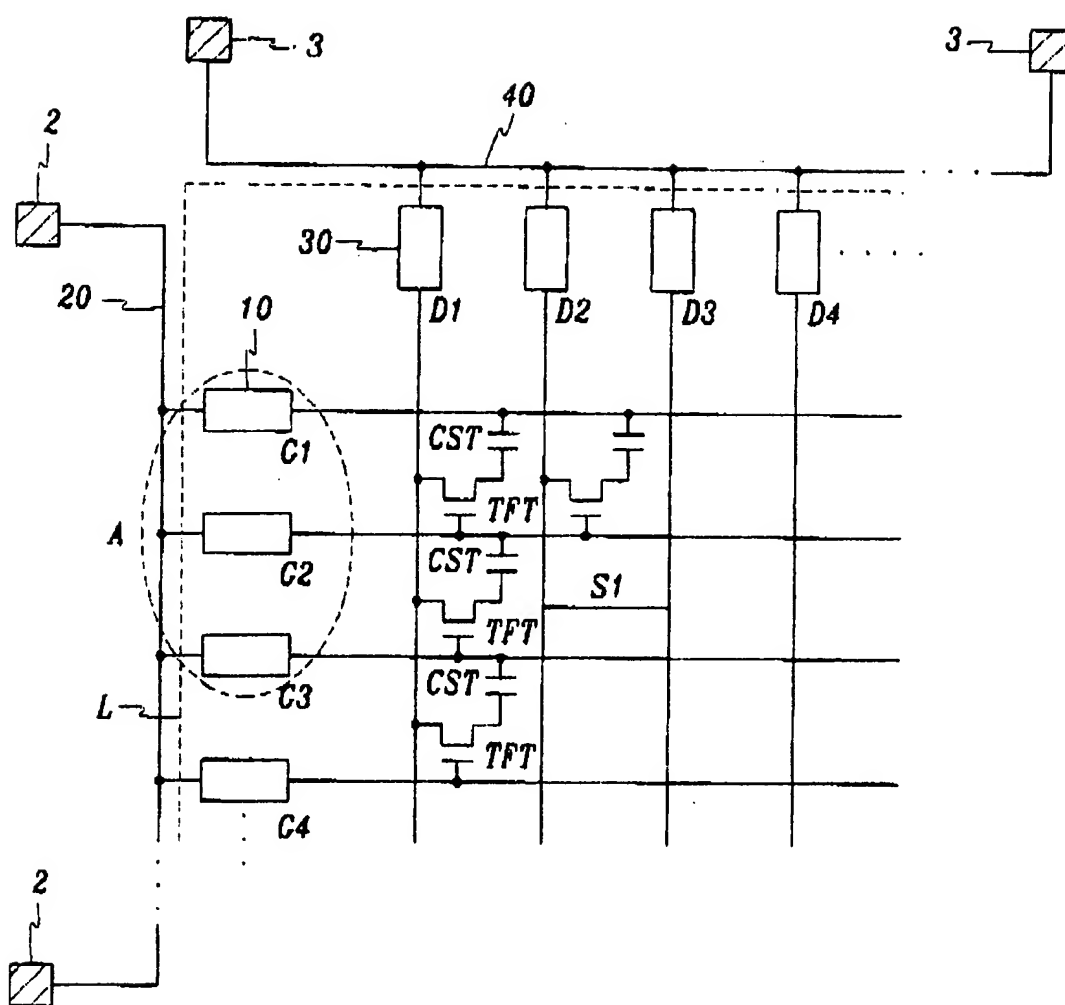


FIG.2(Prior Art)

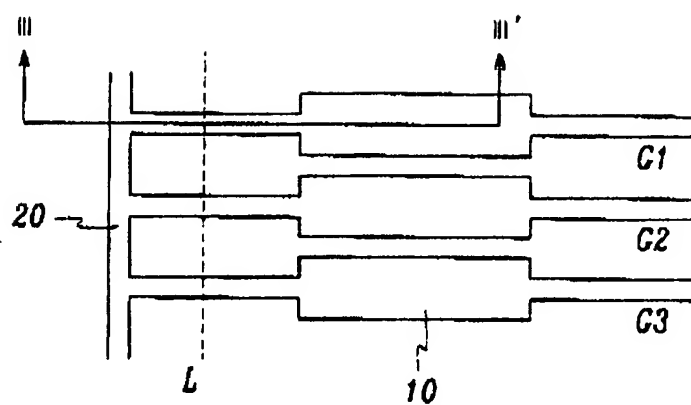


FIG.3(Prior Art)

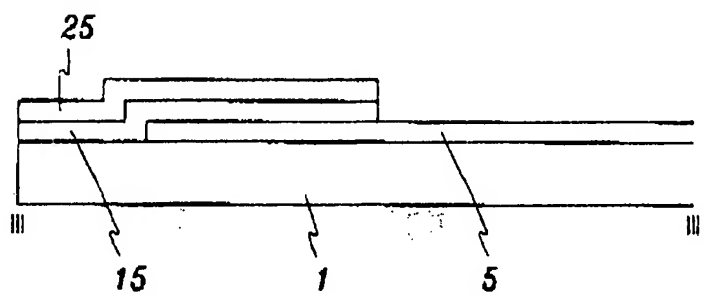
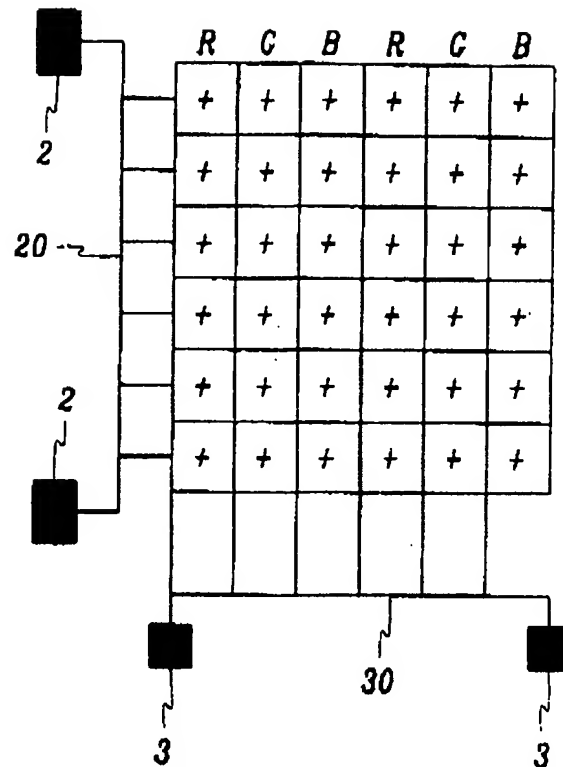


FIG.4(Prior Art)



The diagram illustrates a pixel circuit layout on a grid. The horizontal axis is labeled 'A' and the vertical axis is labeled 'B'. The grid is divided into four quadrants by dashed lines. The top-left quadrant contains a series of four capacitors labeled C1, C2, C3, and C4, with corresponding labels 120, 130, 140, and 150. The top-right quadrant contains a series of four transistors labeled D1, D2, D3, and D4, with corresponding labels 510, 520, 530, and 540. The bottom-left quadrant contains a series of four capacitors labeled C1, C2, C3, and C4, with corresponding labels 120, 130, 140, and 150. The bottom-right quadrant contains a series of four transistors labeled D1, D2, D3, and D4, with corresponding labels 510, 520, 530, and 540. The diagram also shows various other components and labels, including 210, 501, 502, 503, 400, 220, 504, 230, 410, 420, 101, 102, 103, L1, 104, 200, L2, CST, TFT, PX, and S3.

FIG. 6

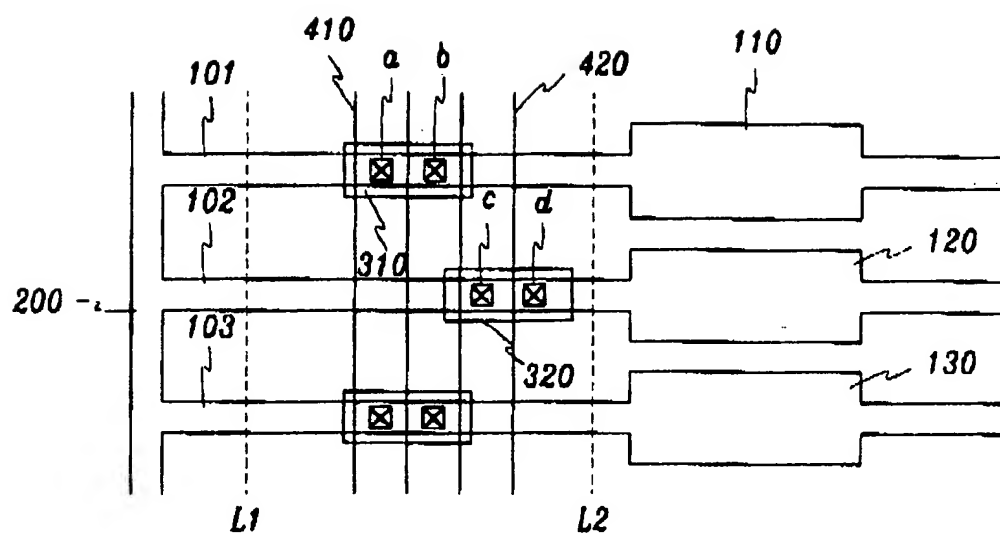


FIG. 7

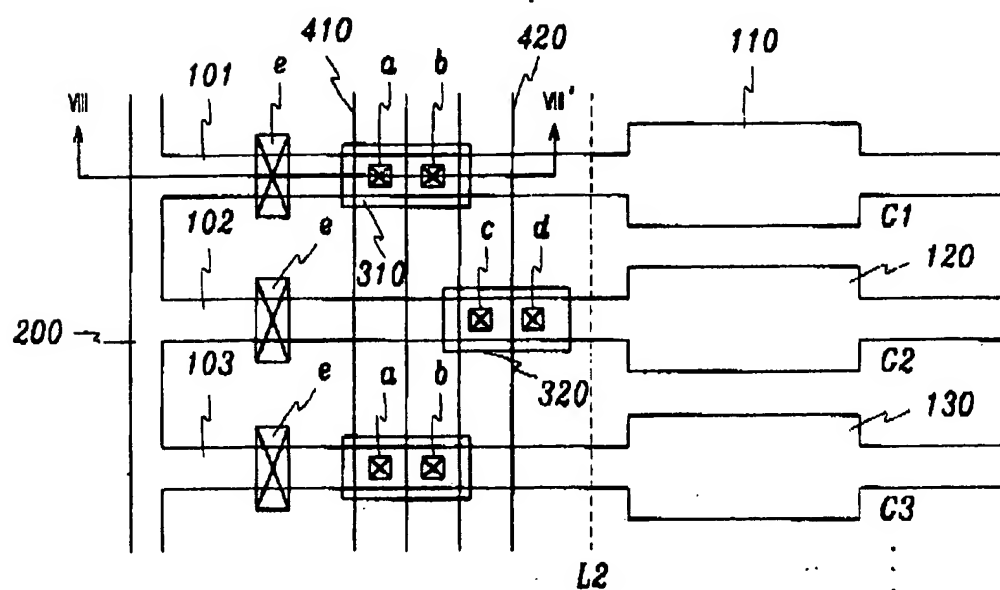


FIG. 9

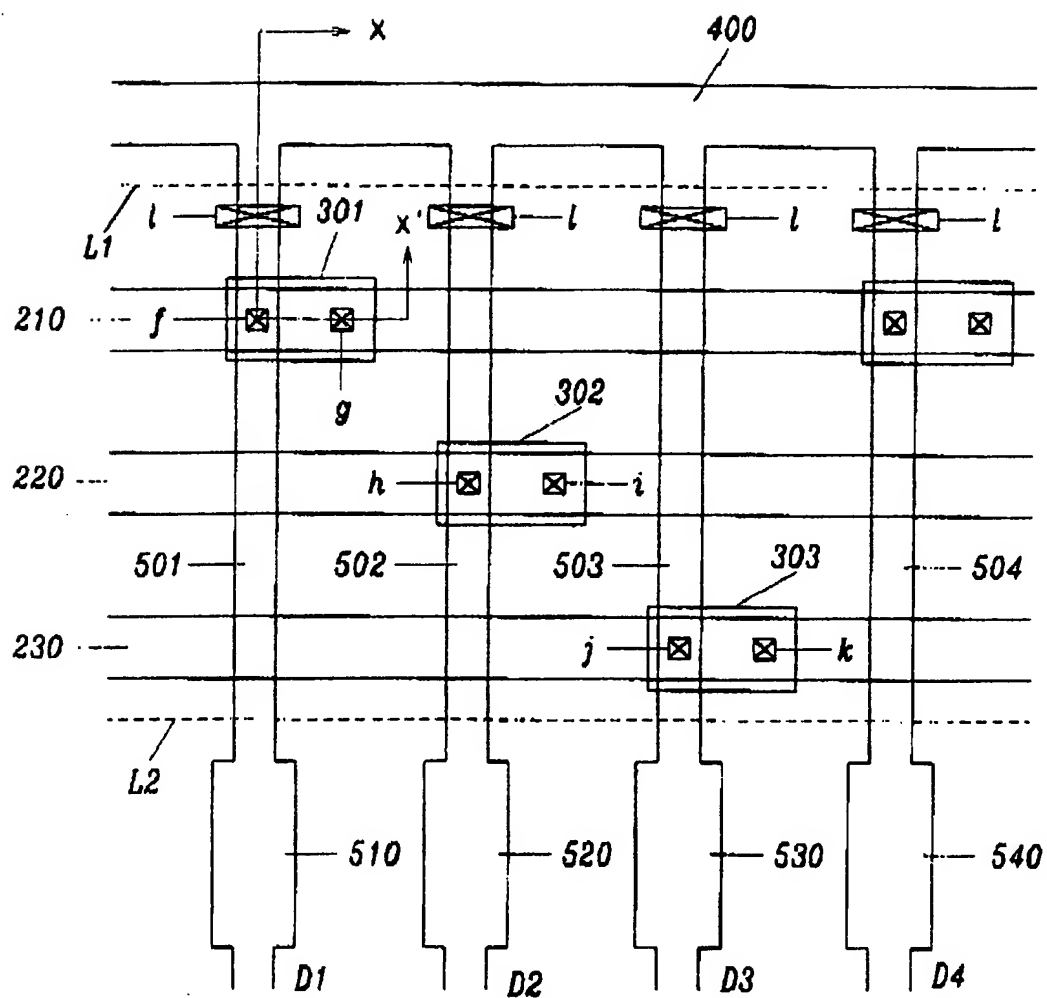


FIG.11A

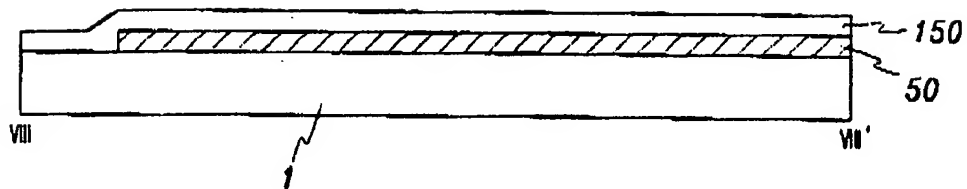


FIG.11B

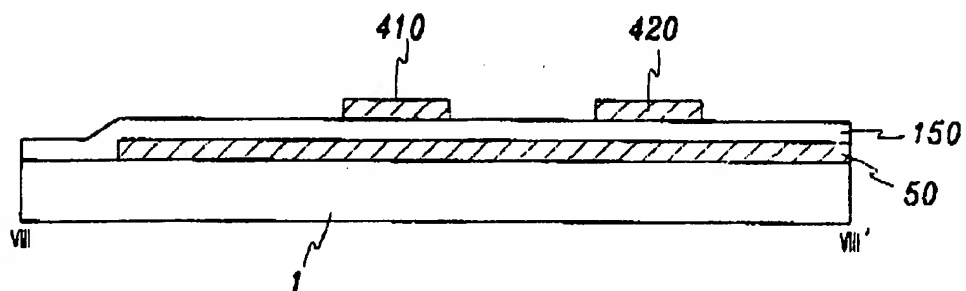


FIG.11C

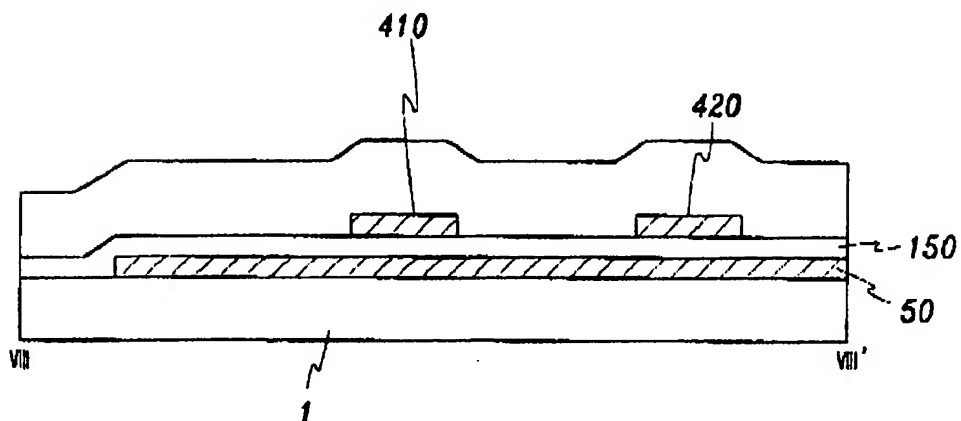


FIG.11D

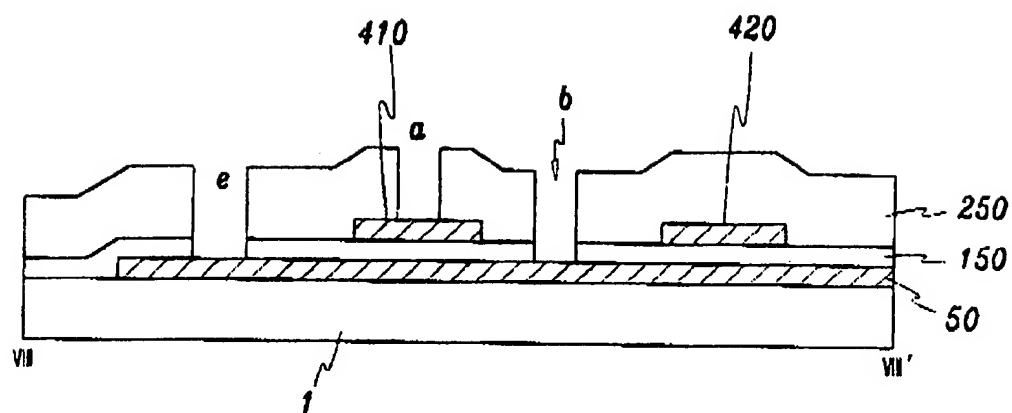


FIG.11E

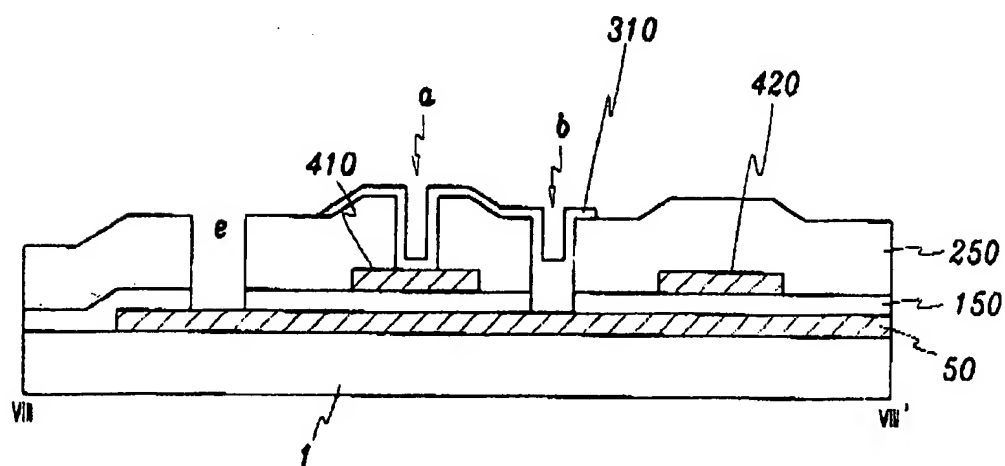


FIG.11F

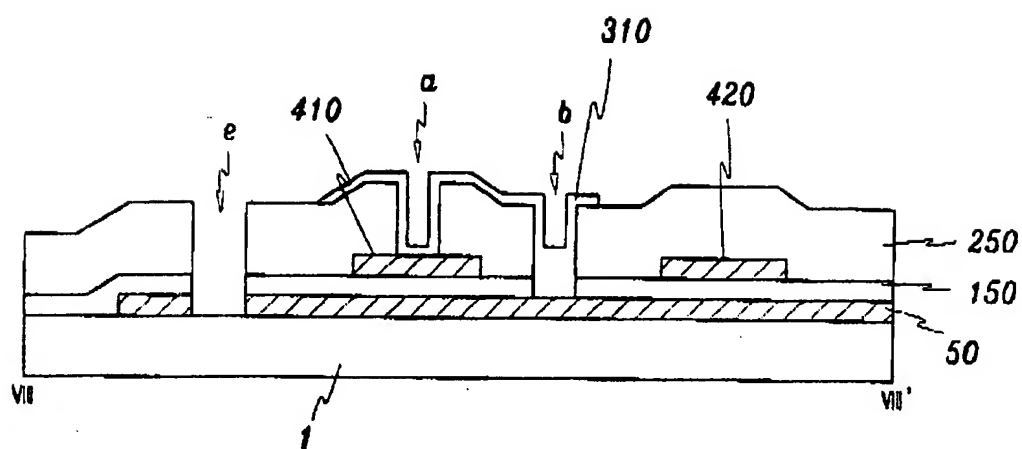


FIG.12A

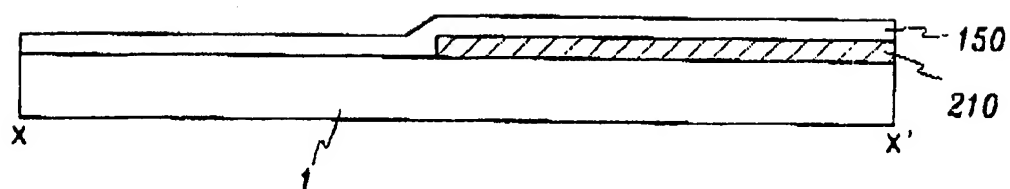


FIG.12B

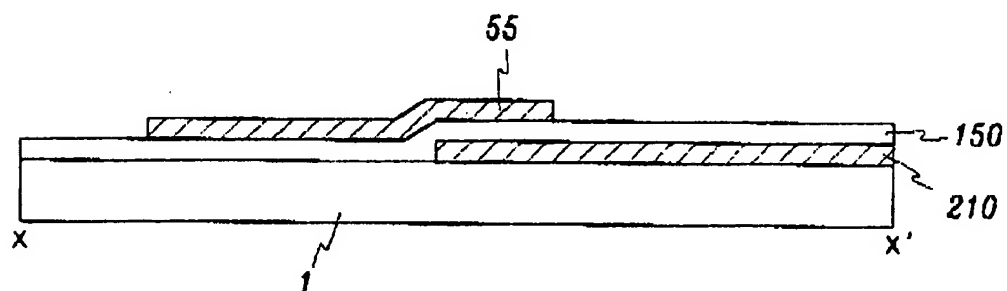


FIG.12C

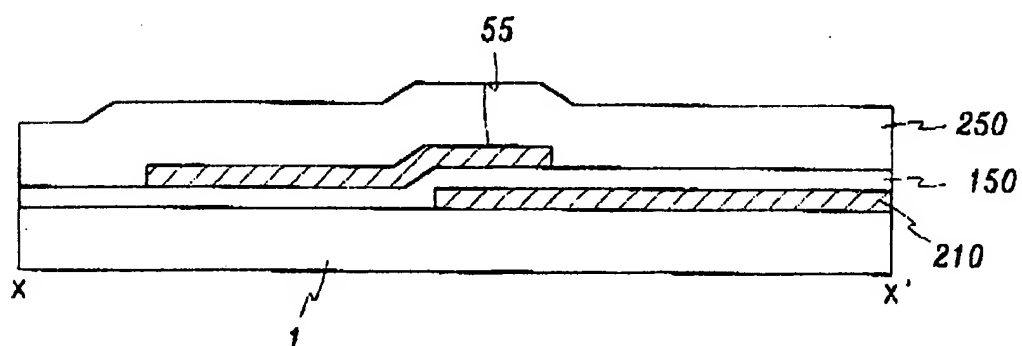


FIG.12D

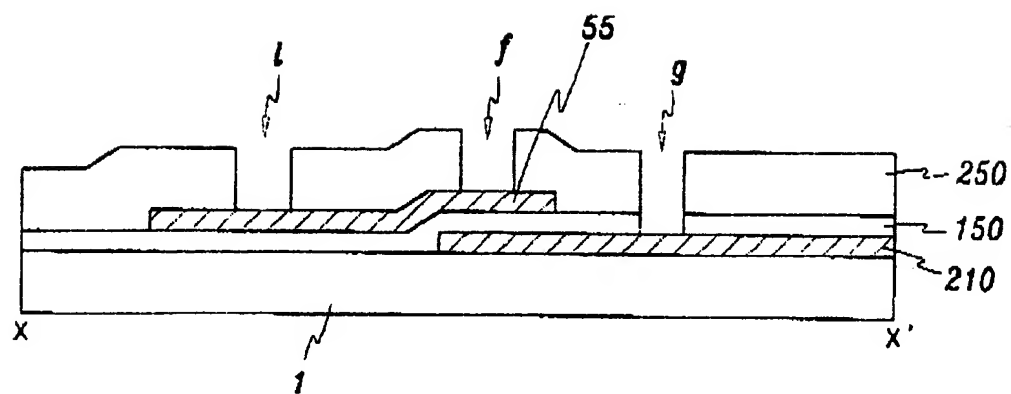


FIG.12E

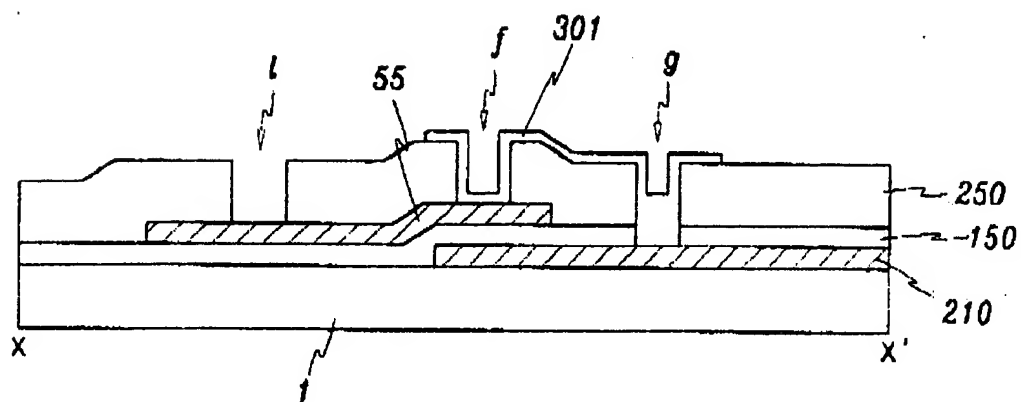


FIG.12F

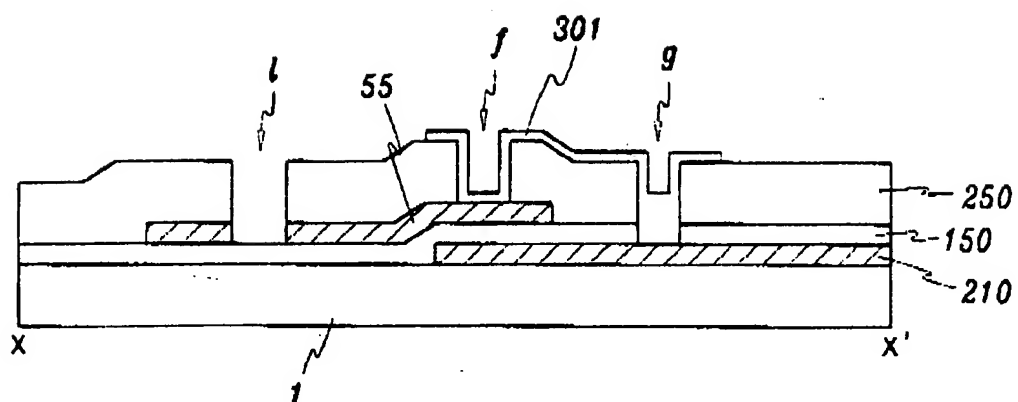


FIG.13

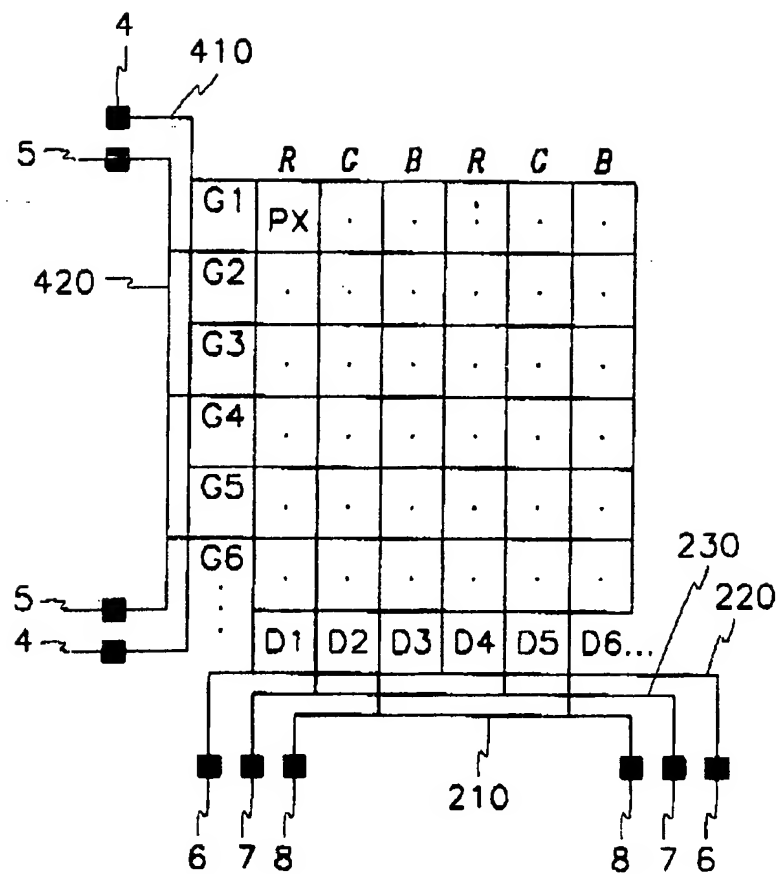


FIG.14

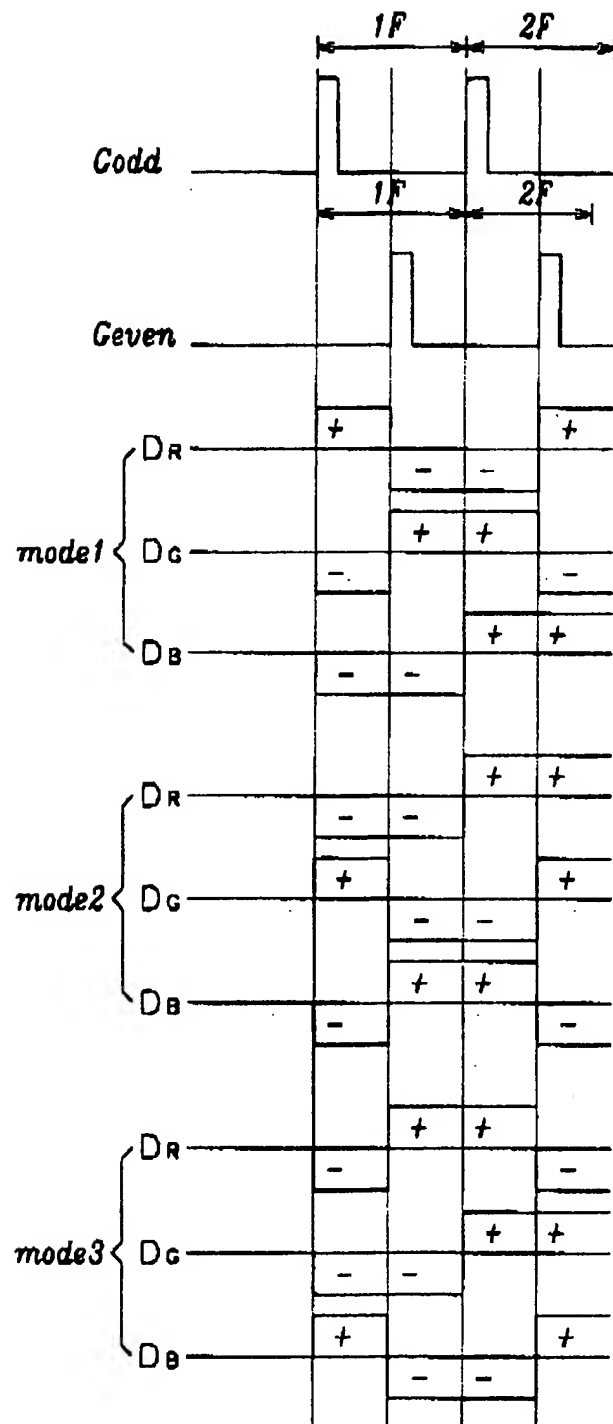


FIG.15A

MODE 1

<i>R</i>	<i>G</i>	<i>B</i>	<i>R</i>	<i>G</i>	<i>B</i>
+	-	-	+	-	-
-	+	-	-	+	-
+	-	-	+	-	-
-	+	-	-	+	-
+	-	-	+	-	-
-	+	-	-	+	-

FIG.15B

MODE 2

<i>R</i>	<i>G</i>	<i>B</i>	<i>R</i>	<i>G</i>	<i>B</i>
-	+	-	-	+	-
-	-	+	-	-	+
-	+	-	-	+	-
-	-	+	-	-	+
-	+	-	-	+	-
-	-	+	-	-	+

FIG.15C

MODE 3

<i>R</i>	<i>C</i>	<i>B</i>	<i>R</i>	<i>C</i>	<i>B</i>
-	-	+	-	-	+
+	-	-	+	-	-
-	-	+	-	-	+
+	-	-	+	-	-
-	-	+	-	-	+
+	-	-	+	-	-

FIG.16

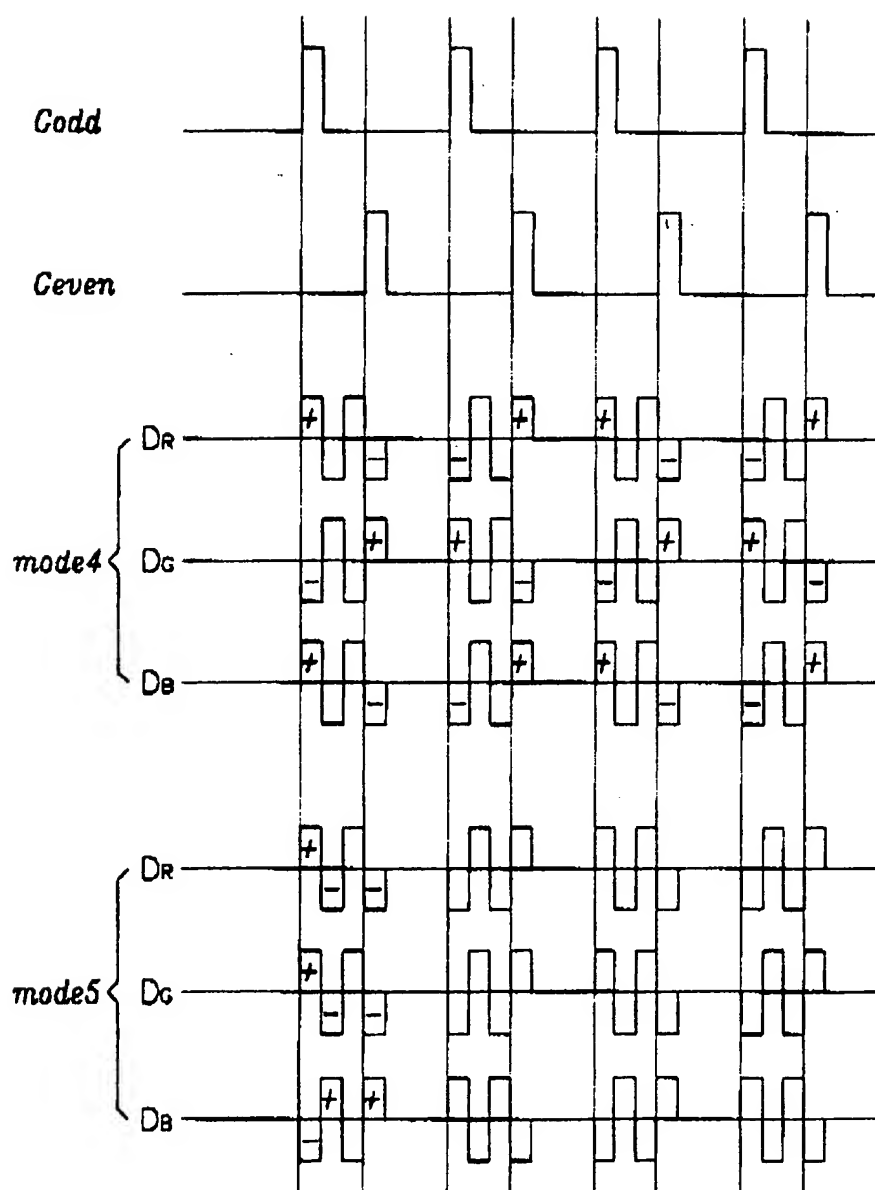


FIG.17A

MODE 4

<i>R</i>	<i>G</i>	<i>B</i>	<i>R</i>	<i>G</i>	<i>B</i>
+	-	+	+	-	+
-	+	-	-	+	-
+	-	+	+	-	+
-	+	-	-	+	-
+	-	+	+	-	+
-	+	-	-	+	-

FIG.17B

MODE 5

<i>R</i>	<i>G</i>	<i>B</i>	<i>R</i>	<i>G</i>	<i>B</i>
+	+	-	+	+	-
-	-	+	-	-	+
+	+	-	+	+	-
-	-	+	-	-	+
+	+	-	+	+	-
-	-	+	-	-	+

FIG.18

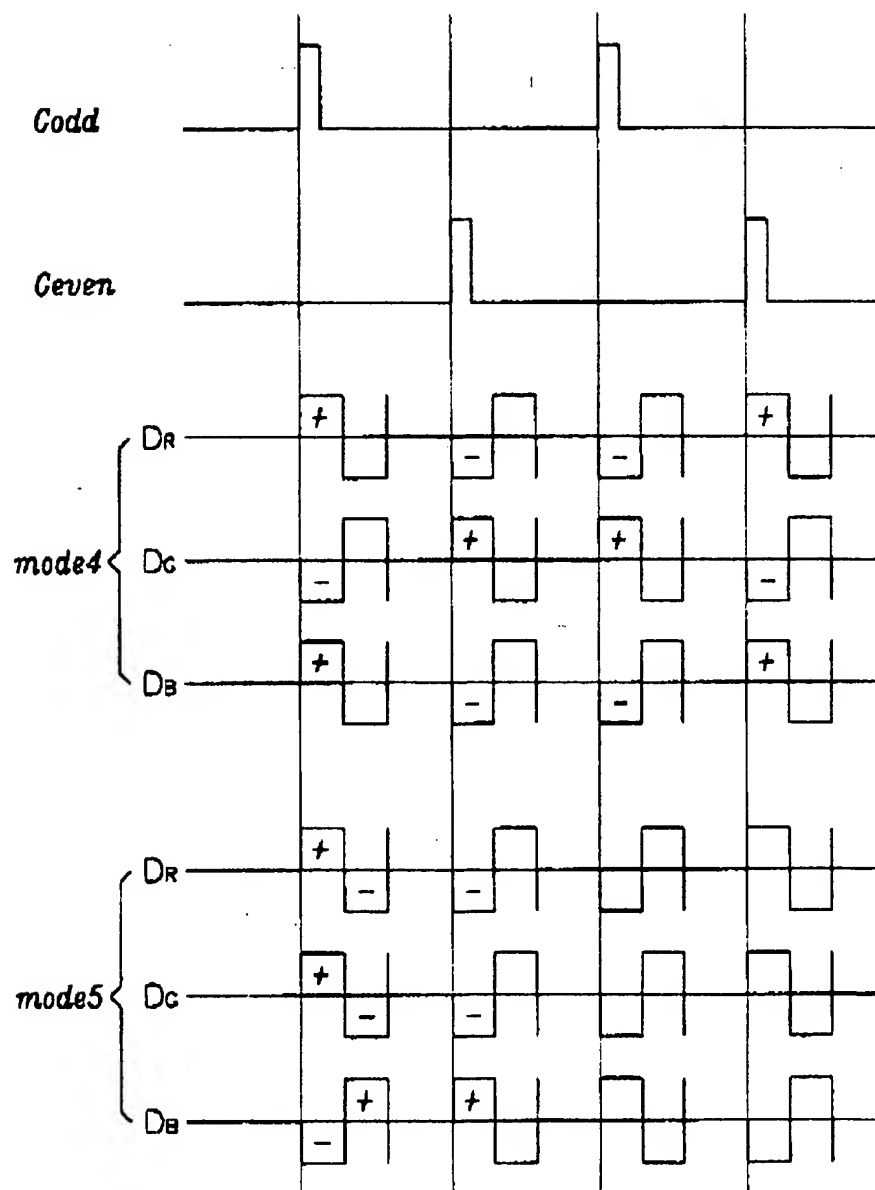


FIG.19

